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U.S. UTILITY Patent Application

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ISSUE DATE

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| **APPLICANTS: Hirano Tomoyuki, Asada Kazumi. | | | | | |
| **CONTINUING DATA VERIFIED: | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: JAPAN P2000-319402 10-19-2000 | | | | | |
| PG-PUB <input type="checkbox"/> | | DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiner's initials | | | | ATTORNEY DOCKET NO 09792909 5225 | |
| TITLE Method of manufacturing semiconductor device | | | | | |

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| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. |
| | | Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Primary Examiner Application Examiner | |
| | | PREPARED FOR ISSUE | |
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